



GAU 2822

- 1 -

Docket No. 0756-1551 ✓

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of: )  
Shunpei YAMAZAKI et al. ✓ )  
Serial No. 08/691,434 ✓ )  
Filed: 08/02/1996 ✓ )  
For: METHOD OF FABRICATING )  
SEMICONDUCTOR DEVICES AND )  
APPARATUS FOR PROCESSING )  
A SEMICONDUCTOR ✓ )

) Art Unit: 2822 ✓  
) Examiner: M. Wilczewski

Y. Coe  
11-2-00  
# IDS  
# 49

TC 2800 MAIL ROOM

OCT 25 2000

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## CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on OCT 18, 2000.

CORRECTION TO INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the reference(s) listed on the attached Form PTO-1449 be made of record in the above-identified application. Copies of the references are submitted herewith in accordance with 37 C.F.R. 1.98(a).

In an Information Disclosure Statement filed August 28, 2000 PTO Form 1449 contained two typographical errors. U.S. Patent 5,620,095 should be 5,620,905, which has been previously submitted and considered. U.S. Patent 5,996,594 should be 5,966,594. Please amend, and consider only the U.S. 5,966,594 patent.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

Respectfully submitted,



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